

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q55026

Tetsuya AKIMOTO, et al.

Appln. No.: 09/347,409

Group Art Unit: 2123

Confirmation No.: 3821 Examiner: DAY, HERNG-DER

Filed: July 6, 1999

For: METHOD AND COMPUTER SOFTWARE PRODUCT FOR CALCULATING AND

PRESENTING A NUMERICAL VALUE REPRESENTATIVE OF A PROPERTY OF A

CIRCUIT (As Amended)

## REQUEST FOR APPROVAL OF PROPOSED DRAWING CORRECTIONS

Commissioner for Patents Washington, D.C. 20231

Sir:

Submitted herewith please find 1 sheet of proposed drawing corrections with the changes indicated in red ink. The Examiner is respectfully requested to acknowledge receipt of the drawing corrections and approve the changes.

Respectfully submitted,

Registration No. 25,665

SUGHRUE MION, PLLC 2100 Pennsylvania Avenue, N.W. Washington, D.C. 20037-3213

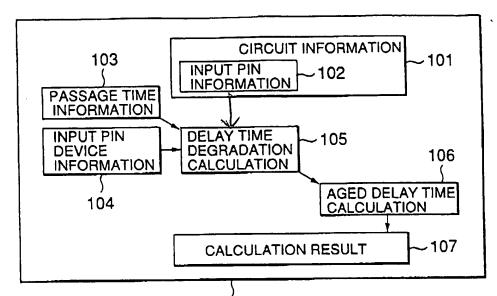
Telephone: (202) 293-7060 Facsimile: (202) 293-7860

Date: August 27, 2002

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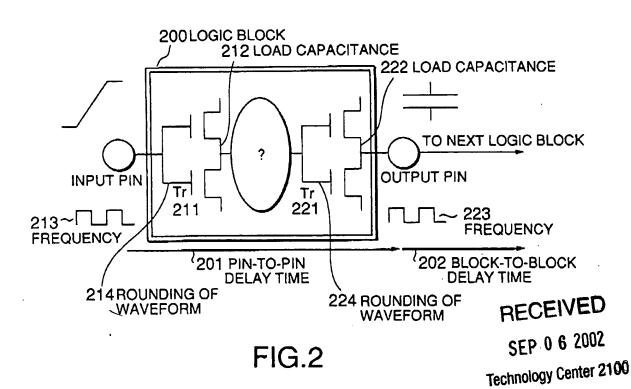
Technology Center 2100

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100 A CONVENTIONAL METHOD OF CALCULATING AGED DELAY TIME OF A LOGIC LEVEL CIRCUIT

## FIG.1 PRIOR ART



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